Solid State System – eMMC 4.51 Controller

Overview

SSS5551 eMMC controller from Solid State System is designed explicitly to support the latest NAND generations from various NAND suppliers. *SSS5551* is fully compliant with JEDEC version 4.51 specifications and backward compatible with earlier version releases. With profound NAND knowledge accumulated throughout the years of storage IC development, *SSS5551* not only bundles with *advanced* ECC design, the completely redesigned firmware algorithm is guaranteed to meet the latest Windows-8® performance
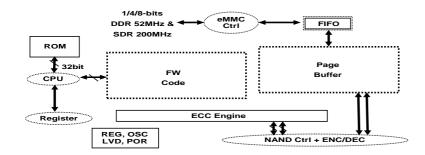requirements set for release by year-end.

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Features

- 32bit Microprocessor
- Compliant with eMMC specification Ver. 4.51
- Advanced ECC supported
- Both Static & Dynamic Wear-leveling supported
- AES 256-bit Encryption supported
- Data bus width modes: 1-bit, 4-bit, and 8-bit supported
- Simple Boot Operation Mode supported
- Sleep mode supported
- Reliable Write supported
- Relay Protected Memory Block supported
- User Data Partition Supported
- Protected Memory Block Supported
- High Priority Interrupt (HPI) Supported
- Different Partition Type Supported
- Dynamic Device Capacity Supported
- ONFI/ Toggle NAND Supported
- MLC NAND supported

Highlights

- eMMC 4.51
- SDR 200Mhz
- Advanced ECC
- Static WL
- Dual Channels
- AES 256-bit Encryption
- Simple Boot Operation
- Reliable Write
- RPMB
- User Data Partition
- Protected Memory Block
- HPI
- Partition Type
- Dynamic Device Capacity
- ONFI/ Toggle NAND
- MLC NAND
- Up to 8CxE

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